

## 2 day Workshop on “Digital System Design Using FPGA”

1<sup>st</sup> & 2<sup>nd</sup> March, 2016

The School of ECE organized two day Workshop “Digital System Design Using FPGA” on 1<sup>st</sup> & 2<sup>nd</sup> March, 2016.. The inaugural session of the workshop was presided by Dr.S L. Pinjare, Prof, School of ECE, REVA University, Bangalore and Dr. R. C. Biradar, Director, School of ECE. The resource person was Mr. Rajeev Kamal, Research Scholar, University Polytechnic of Catalonia, Barcelona, Spain.

Dr. Pinjare spoke about the need of writing synthesizable codes and what the industry requires from young budding engineers. Dr. Biradar also emphasized on the industry based approach that students should have. He also motivated students to be employment generators rather than being only employable, Bangalore being a hub for start ups. Mr.Rajeev Kamal gave a brief insight into what he will covering during the two day session and the importance of those topics viz-a-viz industry.



The students had benefitted from the workshop greatly and they have shown interest in attending more such workshops. The valedictory session was presided by Dr.K.S Gurumurthy , Prof, School of ECE. Mr. Rajeev Kamal was felicitated for his contribution.